

**Amendments to the Specification:**

Please amend the Title as follows:

MEMORY CONTROLLER WITH 1X/MX READ CAPABILITY HAVING RECEIVER CIRCUITRY CAPABLE OF ALTERNATELY GENERATING ONE OR MORE DATA STREAMS AS DATA IS RECEIVED AT A DATA PAD, IN RESPONSE TO COUNTS OF STROBE EDGES RECEIVED AT A STROBE PAD

Please replace paragraph [0001] with the following amended paragraph:

**[0001]** This is a continuation of copending U.S. patent application number 09/828,604 filed on April 7, 2001, now U.S. Patent 6,633,965 issued October 14, 2003, which patent is hereby incorporated by reference herein.